

TAMPER DETECTION SYSTEM
FOR SECURING DATA

Abstract of the Invention

A tamper detection system includes a trigger mechanism system and a
5 detection circuit that operate in combination to protect an integrated circuit
from attack. The detection circuit connects to the trigger mechanism system
that is basically a protective mesh that consists of two separate loops of wire
held in close proximity to each other and an optional ground layer. The mesh
is such that an attempted penetration is highly likely to cause an open of
10 either loop or a short of one loop to the other. The detection system
recognizes shorts and opens in the mesh. The detection circuit is intended to
be very simple in its operation it that it does not require a sophisticated
stimulus and response set to operate. It is designed to be polled on a regular
basis for indications of a change in its operation.

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